CoMET: Composite–Input Magnetoelectric–based Logic Technology

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Abstract-This work proposes CoMET, a fast and energyefficient spintronics device for logic applications. An input voltage is applied to a ferroelectric (FE) material, in contact with a composite structure - a ferromagnet (FM) with in-plane magnetic anisotropy (IMA) placed on top of an intra-gate FM interconnect with perpendicular magnetic anisotropy (PMA). Through the magnetoelectric (ME) effect, the input voltage nucleates a domain wall (DW) at the input end of the PMA-FM interconnect. An applied current then rapidly propagates the DW towards the output FE structure, where the inverse-ME effect generates an output voltage. This voltage is propagated to the input of the next CoMET device using a novel circuit structure that enables efficient device cascading. The material parameters for CoMET are optimized by systematically exploring the impact of parameter choices on device performance. Simulations on a 7nm CoMET device show fast, low-energy operation, with a delay/energy of 99ps/70aJ for INV and 135ps/85aJ for MAJ3.

Index Terms—Design space exploration, magnetoelecric logic, spintronics.

I. INTRODUCTION

Several spin-based devices have been proposed as alternatives to CMOS [9], [11], leveraging spin-transfer torque (STT) [4], [8], [13], [14], switching a ferromagnet (FM) by transferring electron angular momentum to the magnetic moment; spin-Hall effect (SHE), generating spin current from a charge current through a high resistivity material [27]; magnetoelectric (ME) effect [20], using an electric field to change FM magnetization [26], [31]; domain wall (DW) motion through an FM using automotion [12], [26], an external field [7] or current [14], [21], [36]; dipole coupling between the magnets [25]; and propagating spin wave through an FM [2]. In order for the spin-based processor to be running at a CMOS-competitive clock speed of 1GHz, we need the device delay to be around 100 ps. Theoretically, some of the proposed devices can achieve this target delay [11] at the cost of additional energy. However, in order to be competitive with CMOS, spin-based device not only has to be fast, but also energy efficient, i.e., its energy dissipation should be in the range of a few hundred aJ.

We propose CoMET, a novel device that nucleates a DW in an FM channel with perpendicular magnetic anisotropy (PMA), and uses current-driven DW motion to propagate the signal to the output. A voltage applied on an input ferroelectric (FE) capacitor nucleates the DW through the ME effect. For fast, energy-efficient nucleation, we use a composite structure with an IMA–FM layer above the PMA–FM channel. The DW is propagated to the output end of the PMA channel using a charge current applied to a layer of high resistivity material placed under the PMA channel. The inverse–ME (IME) effect induces a voltage at the output end, and we use a novel circuit structure to transmit the signal to the next stage of logic.

The contributions of our work can be summarized as follows:

- The composite structure of IMA–FM/PMA–FM allows DW nucleation under a low applied voltage of 110 mV. Before the application of a voltage, the magnetization in the PMA–FM is moved away from its easy axis by the strong exchange coupling between IMA–FM/PMA–FM, thus enabling a fast low-power DW nucleation.
- We use charge current to realize fast DW propagation through the PMA–FM interconnect. The current-driven DW motion scheme has been experimentally shown to be fast [28], [30], with demonstrated velocities up to 750 m/s. We choose a PMA channel for DW motion, as against one with in-plane magnetic anisotropy (IMA), since it is more robust to DW pinning and surface roughness effects [29], [33].
- A novel circuit structure comprising a dual-rail inverter allows efficient cascading of devices. This scheme improves upon a previous scheme [26] of 6:1 device ratioing and the need for repeated amplifications.
- We explore the design space of the possible PMA– FM material parameters to optimize the performance of the device. Through this systematic design space exploration, we show that it is possible to achieve inverter delay/energy of 99 ps/70 aJ.



Fig. 1. Proposed device concept of CoMET illustrating the composite structure of IMA-FM exchange-coupled with PMA-FM at the input end.

The rest of this paper is organized as follows: In Section II, we explain the operation of CoMET. We present the mathematical models and the simulation framework used in this

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work in Section III. Next, we show the performance of the device as a function of the material parameters in Section IV. Section V concludes the paper.

II. COMET: DEVICE CONCEPT AND OPERATION

The structure of the proposed device is shown in Fig. 1. At the input, a FE capacitor, FE_{in} , is placed atop an IMA– FM. The IMA–FM is exchange–coupled with the input end of a longer PMA–FM interconnect. At its output end, a second FE capacitor, FE_{out} , is placed on top of the PMA– FM interconnect. A layer of high-resistivity spin-Hall material (SHM), which is conducive to strong spin-orbit interaction, is placed beneath the PMA–FM. An oxide layer is present on top of PMA–FM between FE_{in} and FE_{out}.

A. CoMET-based inverter

We explain the device operation in four stages with the help of Fig. 2 and Fig. 3.

Stage 1 – DW nucleation: At time t = 0, an applied voltage, V_{FE} , charges FE_{in}. The resulting electric field across FE_{in}, E_{FE} , may be positive or negative, depending on the sign of V_{FE} , and generates an effective magnetic field, H_{ME} , through the ME effect that couples the electric polarization in FE_{in} with the magnetization in the IMA–FM. This magnetic field acts on the composite structure. For $V_{FE} > 0$, this nucleates a DW in the PMA–FM as seen from Fig. 3(b), with a down–up configuration if the initial magnetization is nucleated. For the opposite case, an up–down configuration is nucleated.



Fig. 2. (a) Graphical representation of the different underlying physical mechanisms of the device. (b) The position of the DW (Q), width (Δ), and phase (ϕ).

If the initial orientation of the PMA–FM is at an angle to the z-axis, a smaller H_{ME} field can nucleate the DW. The composite structure creates this angle due to strong exchange coupling between the IMA–FM and the PMA–FM as can be seen from the magnetization of PMA–FM in Fig. 3(a), thus allowing nucleation under a low magnitude of V_{FE} . In the absence of IMA–FM, voltages up to 1 V are necessary to nucleate a DW whereas we show that with the presence of IMA–FM, voltages as low as 110 mV would suffice.

Stage 2 – DW propagation Once the DW is nucleated in PMA–FM, transistor T_{PROP} is turned on using the signal

 V_{PROP} to send a charge current (J_c) through the SHM. Due to SHE, electrons with opposite spin accumulate in the direction transverse to the charge current as shown in Fig. 2. As a result, a spin current (J_{SHE}) is generated in a direction normal to the plane of SHM. The resultant torque from the combination of SHE and Dzyaloshinskii–Moriya interaction (DMI) [28] at the interface of PMA–FM and the SHM propagates the DW to the output end.

Before the DW reaches the output, V_{RST} turns on transistor T_{RST} to connect FE_{out} to GND as seen from Fig. 3(c). This causes FE_{out} to charge due to the presence of an electric field across it as a result of the IME effect. This step resets the capacitor such that once the DW reaches the output, it can either reverse or maintain the electric polarization of FE_{out}, thus reflecting the result of the operation.

Stage 3 – Output FE switching: The DW reaches the output end in time $t_{propagate}$ as seen from Fig. 3(d) and switches the magnetization of PMA–FM. The magnetization in PMA–FM couples with the electric polarization of FE_{out} through the IME effect. As a result, a voltage, V_{OUT} , is induced at the output node.



Fig. 3. Operation of CoMET device showing (a) steady–state before V_{FE} is applied, (b) nucleation of the DW in PMA–FM after V_{FE} is turned on, (c) propagation of the DW by turning on V_{PROP} , and charging of the output FE capacitor when $V_{RST} > 0$ is applied, and (d) the induction of an output voltage V_{OUT} through the inverse–ME effect.

Stage 4 - Cascading multiple logic stages: Successive logic stages of CoMET can be cascaded as shown in Fig. 4(a), through a dual-rail inverter structure comprising transistors T_P and T_N . A timing diagram showing the application of the different input excitations and the output signal are shown in Fig. 4(b). The signal V_{RST} turns on transistors T_{RST1} and T_{RST2} in the two logic stages to charge the respective FE capacitors. The output voltage induced through the IME effect, V_{OUT1} , turns on either T_N or T_P , depending on its polarity. These transistors form an inverter and set V_{FE} for the next stage to a polarity opposite that of V_{OUT1} . The result of the operation is retained in the PMA-FM when the supply voltage is removed. This allows the realization of nonvolatile logic with CoMET. As a result, the inverter can be powergated after signal transfer, saving leakage. Unlike the charge transfer scheme in [26] with 6:1 ratioing between stages and repeated amplification, our scheme allows all stages to be unitsized, resulting in area and energy efficiency. This scheme also allows efficient charge-based cascading of logic stages as opposed to spin-based cascading, which would require a large number of buffers to overcome the spin losses in the interconnects [15].



Fig. 4. (a) Logic cascading of two CoMET devices using transistors T_P and T_N , and (b) timing diagram showing the application of the V_{FE} , V_{PROP} , and V_{RST} signals.

B. CoMET-based Majority gate

The idea of the CoMET inverter can be extended to build a three-input CoMET majority gate (MAJ3), as shown in Fig. 5(a). The input voltage V_{FE} is applied to each input to nucleate a DW in the PMA–FM below each FE_{in}. The DWs from each input is propagated to the output by turning on T_{PROP} . The DWs compete in the PMA–FM [8], and the majority prevails to switch FE_{out} using the IME effect. Subsequent gates are cascaded using the dual-rail inverter scheme described above.



Fig. 5. (a) CoMET-based three-input majority (MAJ3) gate (b) top view of MAJ3 with the device dimensions marked for a feature size, F and (c) the length (l), width (w), and height (h) of the CoMET device in Fig. 1 considered in this simulation.

III. MODELING AND SIMULATION FRAMEWORK

We now show how the performance of a MAJ3 gate can be modeled. The worst-case delay of this gate occurs when one input differs from the others. At feature size, F, the DW for each input nucleates in PMA-FM below FE_{in} at a distance 2Fonce V_{FE} is applied. The DW from each input then travels a 4F distance to switch FE_{out} as shown in Fig. 5(b). The dimensions of the simulated structure are shown in Fig. 5(c). The IMA-FM aspect ratio (x:y) is set to 2:1 to align the magnetization of PMA-FM at an angle to the easy axis (due to shape anisotropy). The FE_{in} and FE_{out} thicknesses are set to 5 nm to avoid leakage through the capacitors. The PMA-FM thickness is set to 1 nm.

A. Modeling device operation

We analyze the device operation in each of the four stages as follows:

Stage 1 – DW nucleation: The dynamics of electric polarization, \vec{P} , of FE_{in} due to $E_{FE}(=V_{FE}/h_{\text{FE}_{in}})$ as a result of the applied voltage V_{FE} across the thickness of the input FE capacitor, $h_{\text{FE}_{in}}$ are described by the Landau-Khalatnikov (LKh) equation [23]:

$$\gamma_v \frac{\partial P_i}{\partial t} = -\frac{1}{a_{\text{FE}_{in}}} \frac{\partial F_T}{\partial P_i} \tag{1}$$

where F_T is the total free energy of the input structure as a function of E_{FE} , γ_v is the viscosity coefficient, $\underline{P_i}$ is the component of \vec{P} in the *i* direction, and $a_{\text{FE}_{in}}$ is the volume of the input FE capacitor. The resultant \vec{P} generates an effective magnetic field from ME, \vec{H}_{ME} given by,

$$\vec{H}_{ME} = \frac{\kappa_{ME}}{\epsilon_0} \frac{h_{int}}{h_{\text{FE}_{in}}} \vec{P} \tag{2}$$

Here, h_{int} is the ME interface thickness, $h_{\text{FE}_{in}}$ denotes the thickness of FE_{in}, and κ_{ME} refers to the ME coefficient. The magnetic field, H_{ME} is then applied as a Zeeman field to the composite structure in the micromagnetics simulator, OOMMF [22], which solves the Landau-Lifshitz-Gilbert (LLG) equation [16], [34] as shown below, to obtain $t_{nucleate}$:

$$\frac{(1+\alpha^2)}{\gamma}\frac{d\vec{M}}{dt} = -\vec{M}\times\vec{H}_{eff} - \alpha\vec{M}\times(\vec{M}\times\vec{H}_{eff}) \quad (3)$$

Here α refers to the damping constant and \dot{M} denotes the magnetization in PMA–FM. The effective magnetic field, \vec{H}_{eff} is given by:

$$\vec{H}_{eff} = \vec{H}_{ME} + \vec{H}_K + \vec{H}_{demag} + \vec{H}_{ex}$$
(4)

where \vec{H}_K , \vec{H}_{demag} , and \vec{H}_{ex} refer to the contributions to \vec{H}_{eff} from magnetic anisotropy, the demagnetization field, and the exchange field in PMA–FM, respectively.

Stage 2 – DW propagation: The 1D equations that model DW motion describe its instantaneous velocity, dQ/dt and phase ϕ [3], [28] (defined in Fig. 2) through a pair of coupled differential equations:

$$(1+\alpha^{2})\frac{dQ}{dt} = -\gamma\Delta\frac{H_{K}}{2}\sin(2\phi) + (1+\alpha^{2}\beta)B_{STT} +\gamma\Delta\frac{\pi}{2}[\alpha H_{SHE}\cos(\phi) + H_{DMI}\sin(\phi)] (1+\alpha^{2})\frac{d\phi}{dt} = -\gamma\alpha\frac{H_{K}}{2}\sin(2\phi) + \frac{(\beta-\alpha)}{\Delta}B_{STT} +\gamma\frac{\pi}{2}[H_{SHE}\cos(\phi) + \alpha H_{DMI}\sin(\phi)]$$
(5)

The DW width, Δ [19] is given by,

$$\Delta = \frac{\sqrt{A/K_{\rm U,PMA-FM}}}{\sqrt{1 + \frac{\mu_0 M_{\rm S,PMA-FM}}{K_{\rm U,PMA-FM}}} \left[\frac{h_{\rm PMA-FM}}{h_{\rm PMA-FM} + \Delta} - \frac{h_{\rm PMA-FM}}{h_{\rm PMA-FM} + w_{\rm PMA-FM}}\right] \sin^2(\phi)}$$
(6)

whereas the effective field from anisotropy (H_K) , SHE (H_{SHE}) , DMI (H_{DMI}) , and field-like term from STT (B_{STT}) is given by,

$$H_{K} = \frac{2K_{\text{U,PMA-FM}}}{M_{\text{S,PMA-FM}}}; H_{SHE} = \frac{\hbar\theta_{SHE}J_{c}}{2\mu_{0}eM_{\text{S,PMA-FM}}}$$

$$H_{DMI} = \frac{D}{\mu_{0}M_{\text{S,PMA-FM}}\Delta}; B_{STT} = \frac{\mu_{B}P_{\text{PMA-FM}}J_{c}}{eM_{\text{S,PMA-FM}}}$$
(7)

The contribution of B_{STT} to the motion of the DW in PMA– FM is negligible compared to those from SHE and DMI [28]. Here, A, $M_{S,PMA-FM}$, P_{PMA-FM} , h_{PMA-FM} , $K_{U,PMA-FM}$, β , θ_{SHE} , and D refer to the exchange constant, PMA–FM saturation magnetization, PMA–FM polarization ratio, PMA– FM thickness, PMA–FM uniaxial anisotropy, adiabatic STT

parameter, spin-Hall angle, and DMI constant, respectively. The average DW velocity is used to calculate $t_{propagate}$.

 TABLE I

 Simulation parameters used in this work.

Parameter	Value
Viscosity coefficient, γ_v [Vm·s/K] [26]	5.47×10^{-5}
Vacuum permittivity, ϵ_0 [F/m]	8.85×10^{-12}
Vacuum permeability, μ_0 [T·m/A]	1.25×10^{-6}
Charge of the electron, e [C]	1.60×10^{-19}
Gyromagnetic ratio, γ [rad/s·T]	1.76×10^{11}
Speed of light, c [m/s]	3×10^{8}
ME coefficient for FE _{in} , κ_{ME} [s/m] [10]	(0.2/c)
ME coefficient for FE _{out} , κ_{IME} [s/m] [10]	(1.4/c)
Resistivity of SHM, ρ_{SHM} [Ω -m] [28]	1.06×10^{-7}
FE permittivity, ϵ_{FE} [26]	164
Adiabatic STT parameter, β [28]	0.4
DMI constant, $ D $ [mJ/m ²] [3], [28]	0.8
ME interface thickness, h_{int} [nm] [26]	1.5
Transistor threshold voltage, V_{th} [V] [1]	0.2
Bohr magneton, μ_B [J/T]	9.274×10^{-24}
Transistor on-resistance, R_{on} [Ω] [1]	1000
Spin Hall angle, θ_{SHE}	0.5
Spin polarization, P _{PMA-FM} [28]	0.5
Transistor gate capacitance, C_g [fF] [1]	0.1

Stage 3 – Output FE switching: The electric field developed across FE_{out} from IME effect, \vec{E}_{IME} , due to the magnetization, \vec{M} in PMA–FM is used to calculate V_{OUT} as shown below:

$$\vec{E}_{IME} = \kappa_{IME} \frac{h_{int}}{h_{\text{FE}_{out}}} \vec{M};$$

$$V_{OUT} = \vec{E}_{IME} h_{\text{FE}_{out}}$$
(8)

where κ_{IME} is the inverse ME coefficient [10], h_{int} is the interface thickness, and $h_{FE_{out}}$ refers to the thickness of the output FE capacitor.

Stage 4 – Cascading logic stages: The time, $t_{qtransfer}$, required to transfer V_{OUT1} to the input of the next stage includes the delay of the dual-rail inverter and the RC delay of the wire from the inverter output to FE_{in} of the next stage.

B. Modeling performance parameters

The delay and energy of a K-input CoMET majority gate are:

$$T_{\text{COMET}} = 2(t_{nucleate} + t_{propagate} + t_{qtransfer})$$

$$E_{\text{COMET}} = 2(E_{FE} + E_{TX} + E_{Joule} + E_{leakage})$$
(9)

where E_{FE} , E_{TX} , E_{Joule} , and $E_{leakage}$, respectively, refer to the energy for charging the FE_{in}, turning the transistors on, SHM Joule heating, and due to transistor leakage currents. The factor of 2 is due to PMA–FM magnetization initialization of each input to a state that allows DW nucleation [26]. Finally,

$$E_{TX} = (C_g/2) \left((K+1)V_{RST}^2 + V_{PROP}^2 + 2V_{OUT}^2 \right);$$

$$E_{Joule} = (J_c w_{SHM} t_{SHM})^2 [R_{on} + R_{SHM}] t_{propagate};$$

$$E_{FE} = (K/2)C_{FE_{in}}V_{FE}^2 ; R_{SHM} = (\rho_{SHM} l_{SHM})/(w_{SHM} t_{SHM})$$



Fig. 6. Layout of a CoMET-based three-input majority gate.

Here, C_g , $C_{\text{FE}_{in}}$, R_{on} , and R_{SHM} refer to the transistor gate capacitance, capacitance of the input FE capacitor, transistor on–resistance, and resistance of the SHM, respectively. The length, width, and thickness of the SHM, are respectively, given by l, w, and h, each with subscript SHM.

C. Layout of CoMET-based majority gate

The layout of MAJ3 corresponding to the schematic shown in Fig. 5 for a chosen value of F, is shown in Fig. 6. We draw the layout according to the design rules for F as described in detail in [9]. The reset transistor for each input FE capacitor i, $T_{RST,i}$, with $i \in 1, 2, 3$, and the reset transistor for the output FE capacitor, $T_{RST,o}$, are local to the majority gate as shown in the layout. The transistor required to send a charge current through the SHM, T_{PROP} , is shared globally by multiple gates. The dual-rail inverter is local to the majority gate and transfers the information to the next stage. The area of the MAJ3 gate is $29F \times 16F$ nm².

IV. RESULTS AND DISCUSSION

The delay of the device is a function of the dimensions of IMA–FM and PMA–FM material parameters, specifically $M_{\text{S,PMA-FM}}$, $K_{\text{U,PMA-FM}}$, A, and α . We explore the design space consisting of the combination of these parameters and analyze their impact on device performance. We demonstrate the results of the design space exploration for F = 15 nm and show two sample design points for F set to 15 nm and 7 nm.

A. Choice of material parameters

The simulation parameters and their values used in this work are listed in Table I. The parameter space is chosen to reflect realistic values: the choice of $A \in \{10 \text{ pJ/m}, 20 \text{ pJ/m}, 30 \text{ pJ/m}, 40 \text{ pJ/m}\}$ is chosen to reflect the typical exchange constant of existing and exploratory ferromagnetic materials. Lowering *A* further would make the Curie temperature too low [24]. The choice of $M_{\text{S,PMA-FM}} \in \{0.3 \times 10^6 \text{ A/m}, 0.4 \times 10^6 \text{ A/m}, 0.5 \times 10^6 \text{ A/m}\}$ and

 $K_{\text{U,PMA-FM}} \in \{0.5 \times 10^6 \text{ J/m}^3, 0.6 \times 10^6 \text{ J/m}^3, 1 \times 10^6 \text{ J/m}^3\}$ allow the mapping of PMA-FM materials to existing materials. The choice of $\alpha \in \{0.01, 0.05, 0.08, 0.1\}$ is **T** 1*F* free of any constraint to material mapping as it can be modified by adequately doping the PMA-FM [32], [35]. The saturation magnetization of the IMA-FM, $M_{\text{S,IMA-FM}}$ is set to $1 \times 10^6 \text{ A/m}$. The exchange constant and the damping constant for the IMA-FM is set to the same value as that of PMA-FM.

B. DW nucleation

We estimate $t_{nucleate}$ in OOMMF when the DW nucleates beneath the IMA–FM as shown in the snapshots in Fig. 7(a). We first relax the composite structure in OOMMF for 200 ps before applying the effective ME field as a Zeeman field. This time period allows the PMA–FM to reach an equilibrium state before the DW is nucleated. In a typical circuit, this state could be achieved by the PMA–FM in the time interval between successive switching activity. At the end of 200 ps, denoted in the figure as $t_1 = 0$ ps, the magnetization of the PMA–FM rests at an angle to the easy axis owing to the strong exchange coupling with the IMA–FM situated on top. After applying a Zeeman field, the DW nucleates in PMA–FM at 2F after a delay of 44 ps.

We compare the voltages required to nucleate the DW in the PMA-FM at approximately the same $t_{nucleate}$, in the absence of the IMA-FM on top of the PMA-FM to provide the initial angle. The procedure to calculate $t_{nucleate}$ is identical to the experiment in Fig. 7(a). We perform this analysis for two cases: (i) when the applied Zeeman field acts on a region $2F \times 1F \times 1$ nm corresponding to the scenario shown in Fig. 7(b). The DW nucleates at $t_{nucleate} = 44 \text{ ps}$ at 2F. However, V_{FE} required to generate the DW is now $350 \,\mathrm{mV}$. After relaxing the magnetization for $200 \,\mathrm{ps}$, an absence of $\overline{\mathrm{IMA-}}$ FM translates to a very low initial angle at $t_1 = 0$ ps which necessitates a stronger effective ME field, H_{ME} , and therefore a higher V_{FE} to nucleate the DW for a given $t_{nucleate}$. (ii) The absence of IMA-FM allows us to further compact the CoMET device such that the FE capacitor dimensions are the minimum possible at a chosen value of F. This corresponds to the dimensions, $1F \times 1F \times 5$ nm (as opposed to those shown in Fig. 5(c)), the region from the left end of PMA-FM on which H_{ME} acts. We find that the voltage required to nucleate the DW at 1F, as shown in Fig. 7(c), is close to 1 V. From these two experiments, we conclude that the composite structure facilitates a fast and at the same time, an energy-efficient DW nucleation.

The nucleation of DW in the PMA-FM is not only a function of PMA-FM material parameters, but also depends on the material dimensions of the IMA-FM. As stated in Section III, the aspect ratio of the IMA-FM is set to 2:1 to obtain the shape anisotropy necessary for the coupling with PMA-FM. We then explore the dependence of $t_{nucleate}$ on the thickness of IMA-FM, h_{IMA-FM} and plot the results in Fig. 8. As h_{IMA-FM} increases, it becomes harder to switch the PMA-FM due to strong exchange coupling between IMA-FM and PMA-FM, increasing $t_{nucleate}$. We therefore select $h_{IMA-FM} = 1 \text{ nm}$.

The impact of material parameters of PMA–FM on $t_{nucleate}$ is shown in Fig. 9(a) and Fig. 9(b) for $V_{FE} = 110 \text{ mV}$ and

Fig. 7. DW nucleation in PMA–FM (a) with the composite structure used in this work (b) without the composite structure, i.e., without the IMA–FM with the ME field applied for a region $2F \times 1F \times 1$ m from the left end of PMA–FM and (c) without the composite structure with the ME field applied for a region $1F \times 1F \times 1$ m from the left end of PMA–FM. In (a) the red region refers to the IMA–FM, and the blue region refers to PMA–FM in (a), (b), and (c). The material parameters used in the OOMMF simulation are: $M_{S,PMA-FM} = 0.5 \times 10^6 \text{ A/m}$, $K_{U,PMA-FM} = 0.6 \times 10^6 \text{ J/m}^3$, A = 10 pJ/m, $\alpha = 0.01$. The voltages required to nucleate the DW at $t_{nucleate} = 44 \text{ ps}$ corresponding to (a) $V_{FE} = 110 \text{ mV}$, (b) $V_{FE} = 350 \text{ mV}$ and (c) $V_{FE} = 1.06 \text{ V}$.



Fig. 8. Nucleation delay, $t_{nucleate}$, of the CoMET device for F=15 nm, as a function of the IMA–FM thickness, $h_{\rm IMA-FM}$. The PMA–FM material parameters used in the OOMMF simulation are: $M_{\rm S,PMA-FM}=0.3\times10^6$ A/m, $K_{\rm U,PMA-FM}=0.5\times10^6$ J/m³, A=10 pJ/m and $\alpha=0.05$ (similar trends are seen for other parameter choices).

 $V_{FE} = 150 \text{ mV}$, respectively. It is seen that (a) a larger V_{FE} reduces $t_{nucleate}$, and this can be shown to be consistent with the DW nucleation Equations (1–3). A larger V_{FE} corresponds to a larger E_{FE} across FE_{in}, which in turn creates a larger H_{ME} to nucleate the DW faster. (b) Lower values of H_K are more conducive to nucleation; a lower anisotropy field makes it easier for H_{ME} to switch the magnetization between the two easy axes and (c) low values of A reduce $t_{nucleate}$ owing to the weaker exchange coupling with the neighboring magnetic domains of the PMA–FM. We note that for A > 10 pJ/m, the number of design points at which the nucleation does not occur increases. Therefore we pick the lowest value of A = 10 pJ/m. This choice does not restrict the search space for DW propagation as $t_{propagate}$ is primarily dictated by the choice of $M_{S,PMA-FM}$.

C. DW propagation

With this choice, we show $t_{propagate}$ for $J_c = 10^{11}$ A/m² and 10^{12} A/m² in Figs. 10(a) and (b), respectively. Increasing J_c increases the torque from SHE as seen from the expressions for H_{SHE} in Equation 7. This can be seen from Fig. 10(c) where increasing J_c increases the DW velocity, thereby reducing $t_{propagate}$. These curves lie in three clusters, and show the dominance of $M_{S,PMA-FM}$ over other parameters.

This can also be seen from Figs. 10(a) and (b) where the lowest $t_{propagate}$ bars ($H_K = 6.7 \text{ T}$) correspond to $M_{\text{S,PMA-FM}} = 0.3 \times 10^6 \text{ A/m}$. This is consistent with the Equation 7: a lower $M_{\text{S,PMA-FM}}$ implies higher H_{SHE} and H_{DMI} , and therefore higher DW velocity.



Fig. 9. Nucleation delay, $t_{nucleate}$, of the CoMET device for F = 15 nm as a function of (a) material parameters for $V_{FE} = 110 \text{ mV}$ and (c) material parameters for $V_{FE} = 150 \text{ mV}$. The triangles indicate successful nucleation and while the circles indicate unsuccessful nucleation.

D. Performance evaluation

For the three $M_{\text{S,PMA}}$ corresponding to each of the three clusters in Fig. 10(c), we plot T_{CoMET} vs. E_{CoMET} for MAJ3 in Fig. 11 for the two values of V_{FE} . The dual-rail inverter delay, $t_{qtransfer}$, is calculated using the PTM technology models [1]. For a chosen $M_{\text{S,PMA-FM}}$ and V_{FE} , the energy-delay data points are obtained by increasing J_c from 10^{10} A/m^2 to 10^{12} A/m^2 in discrete steps. The main observations from Fig. 11 are as follows:

- Increasing V_{FE} is seen to reduce T_{CoMET} by reducing $t_{nucleate}$, at the expense of a larger E_{CoMET} .
- A higher J_c corresponds to lower T_{CoMET} , but E_{CoMET} is only marginally higher since it is primarily dominated by the transistor energy.
- Initially when J_c increases, T_{CoMET} reduces at the same rate as J_c , thus keeping the energy approximately constant. After a certain point, increasing J_c only gives



Fig. 10. DW propagation delay, $t_{propagate}$, of the CoMET device for F = 15 nm as a function of the material parameters for A = 10 pJ/m with (a) $J_c = 10^{11} \text{ A/m}^2$, (b) $J_c = 10^{12} \text{ A/m}^2$ and (c) DW velocity as a function of the current density, J_c , for all of the design points shown in (a) and (b). Note that points on the x and y axes of the bar chart in (a) and (b) are not equally spaced.



(b)

Fig. 11. Energy vs. delay of the CoMET-based MAJ3 gate for three design points, corresponding to the $M_{\rm S,PMA-FM}$ values for the clusters in Fig. 10(c) for (a) V_{FE} . Other parameter values: $\alpha = 0.01$, $A = 10 \, {\rm pJ/m}$, $K_{\rm U,PMA-FM} = 0.5 \times 10^6 \, {\rm J/m}^3$.

marginal improvements in delay. This result is consistent with Fig. 10(c); as J_c increases from 10^{10} A/m² to 10^{12} A/m², DW velocity increases sharply initially but only increases gradually later.

• A robust design point can be chosen such that T_{CoMET} is less variable with material parameters. This corresponds to the right portion of each curve where the delay only improves marginally with increase in J_c .

The best (T_{CoMET} , E_{CoMET}) for each V_{FE} for MAJ3/INV for F = 15 nm and F = 7 nm are shown in Fig. 12(a) and (b), respectively. It can be seen that $t_{propagate}$ dominates T_{CoMET} while E_{CoMET} is dominated by energy associated with turning

V _{FE} (mV)	t _{nucleate} (ps)	t _{propagate} (ps)	t _{qtransfer} (ps)	T _{CoMET} (ps)
110	35/35	77.4/38.7	8.8/8.8	242.4/165.5
150	30/30	77.4/38.7	8.2/8.2	231.2/153.8

V_{FE} (mV)	E _{FE} (aJ)	<i>E</i> _{<i>TX</i>} (aJ)	E _{joule} (aJ)	E _{leakage} (aJ)	E _{CoMET} (aJ)
110	2.4/0.8	33.6/24.2	6.2/3.1	16.3/16.3	117.0/88.8
150	4.4/1.5	42.0/30.6	6.2/3.1	22.8/22.8	150.8/116.0

(a)				
$\begin{bmatrix} V_{FE} \\ (\text{mV}) \end{bmatrix}$	t _{nucleate} (ps)	t _{propagate} (ps)	t _{qtransfer} (ps)	T _{CoMET} (ps)
110	30/30	36.2/18.1	7.9/7.9	148.2/112.0
150	25/25	36.2/18.1	6.2/6.2	134.8/98.6

V_{FE} (mV)	E _{FE} (aJ)	<i>E_{TX}</i> (aJ)	E _{joule} (aJ)	E _{leakage} (aJ)	E _{CoMET} (aJ)
110	0.5/0.1	16.8/12.0	2.1/0.7	13.7/13.7	66.2/53.0
150	0.9/0.3	21.4/15.3	2.1/0.7	18.5/18.5	84.6/69.6

(b)

Fig. 12. Delay and energy of CoMET-based MAJ3/INV gate for (a) F = 15 nm and (b) F = 7 nm for the design point corresponding to parameters, $M_{\text{S,PMA-FM}} = 0.3 \times 10^6 \text{ A/m}$, $K_{\text{U,PMA-FM}} = 0.5 \times 10^6 \text{ J/m}^3$, $J_c = 5 \times 10^{11} \text{ A/m}^2$, $\alpha = 0.01$, and A = 10 pJ/m.

the transistors on and the corresponding leakage. The delay and energy obtained using the CMOS technology given respectively by (T_{CMOS} , E_{CMOS}) for an inverter is (1.8 ps, 38.7 aJ) at F = 15 nm and (1.6 ps, 19.8 aJ) at F = 7 nm. For CMOSbased MAJ3 gate, the performance numbers are (14.8ps, 704.2aJ) at F = 15 nm and (11.4 ps, 361.6 aJ) at F = 7 nm. The CMOS performance numbers were obtained using the PTM technology models [1] at nominal supply voltages of 0.85 V for F = 15 nm and 0.7 V for F = 7 nm. Thus we see that a MAJ3 gate can be implemented more energy-efficiently with CoMET than with CMOS.

At these design points, $M_{\text{S,PMA-FM}}$, $K_{\text{U,PMA-FM}}$, and A can be mapped to MnGa–based Heusler alloy [6], [20]. The damping constant, $\alpha = 0.01$ can be engineered by choosing a new composition of PMA–FM. For the FE layer, BiFeO₃

(BFO) can be used [26], while the SHM could be β -W, Pt, β -Ta [5], [17], [18] or some new materials under investigation.

V. CONCLUSION

A novel spintronic logic device based on magnetoelectric effect and fast current–driven domain wall propagation has been proposed. We have shown that the composite input structure of a FM with IMA placed in contact with a PMA–FM allows circuit operation at low voltages of 110 mV and 150 mV. A novel circuit structure comprising a dual–rail inverter structure for efficient logic cascading has also been introduced. The impact of the different material parameters on the performance of the device is then systematically explored. An optimized INV has a delay of 98.6 ps with energy dissipation of 69.6 aJ at 7nm, while a MAJ3 gate runs at 134.8 ps and 84.6 aJ.

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REFERENCES

- "Predictive Technology Model," http://ptm.asu.edu, accessed: 2016-08-09.
- [2] A. Khitun and K. L. Wang, "Nano scale computational architectures with spin wave bus," *Superlattices and Microstructures*, vol. 38, no. 3, pp. 184–200, 2005.
- [3] A. Thiaville, S. Rohart, E. Jué, V. Cros, and A. Fert, "Dynamics of Dzyaloshinskii domain walls in ultrathin magnetic films," *Europhysics Letters*, vol. 100, no. 5, pp. 57002–1–57002–6, Dec 2012.
- [4] B. Behin-Aein, D. Datta, S. Salahuddin, and S. Datta, "Proposal for an all-spin logic device with built-in memory," *Nature Nanotechnology*, vol. 5, no. 4, pp. 266–270, Feb 2010.
- [5] C. F. Pai, L. Liu, Y. Li, H. W. Tseng, D. C. Ralph, and R. A. Buhrman, "Spin transfer torque devices utilizing the giant spin Hall effect of Tungsten," *Applied Physics Letters*, vol. 101, no. 12, pp. 122404–1– 122404–4, Sep 2012.
- [6] C. L. Zha, R. K. Dumas, J. W. Lau, S. M. Mohseni, S. R. Sani, I. V. Golosovsky, Å. F. Monsen, J. Nogus, and J. Åkerman, "Nanostructured MnGa films on Si/SiO₂ with 20.5 kOe room temperature coercivity," *Journal of Applied Physics*, vol. 110, no. 9, pp. 093 902–1–093 902–4, Nov 2011.
- [7] D. A. Allwood, G. Xiong, C. C. Faulkner, D. Atkinson, D. Petit, and R. P. Cowburn, "Magnetic domain-wall logic," *Science*, vol. 309, no. 5741, pp. 1688–1692, Sep 2005.
- [8] D. E. Nikonov, G. I. Bourianoff, and T. Ghani, "Proposal of a spin torque majority gate logic," *IEEE Electron Device Letters*, vol. 32, no. 8, pp. 1128–1130, Aug 2011.
- [9] D. E. Nikonov and I. A. Young, "Overview of beyond–CMOS devices and a uniform methodology for their benchmarking," *Proceedings of the IEEE*, vol. 101, no. 12, pp. 2498–2533, Dec 2013.
- [10] D. E. Nikonov and I. A. Young, "Benchmarking spintronic logic devices based on magnetoelectric oxides," *Journal of Materials Research*, vol. 29, pp. 2109–2115, Sep 2014.
- [11] D. E. Nikonov and I. A. Young, "Benchmarking of beyond–CMOS exploratory devices for logic integrated circuits," *IEEE Journal on Exploratory Solid–State Computational Devices and Circuits*, vol. 1, pp. 3–11, Dec 2015.
- [12] D. E. Nikonov, S. Manipatruni, and I. A. Young, "Automotion of domain walls for spintronic interconnects," *Journal of Applied Physics*, vol. 115, no. 21, pp. 213 902–1–213 902–5, 2014.
- [13] D. M. Bromberg, D. H. Morris, L. Pileggi, and J. G. Zhu, "Novel STT-MTJ device enabling all-metallic logic circuits," *IEEE Transactions on Magnetics*, vol. 48, no. 11, pp. 3215–3218, Nov 2012.
- [14] J. A. Currivan, Y. Jang, M. D. Mascaro, M. A. Baldo, and C. A. Ross, "Low energy magnetic domain wall logic in short, narrow, ferromagnetic wires," *IEEE Magnetics Letters*, vol. 3, pp. 3 000 104–1–3 000 104–4, Apr 2012.
- [15] J. Kim, A. Paul, P. A. Crowell, S. J. Koester, S. S. Sapatnekar, J. P. Wang, and C. H. Kim, "Spin-based computing: Device concepts, current status, and a case study on a high-performance microprocessor," *Proceedings* of the IEEE, vol. 103, no. 1, pp. 106–130, Jan 2015.

- [16] L. D. Landau and E. Lifshitz, "On the theory of the dispersion of magnetic permeability in ferromagnetic bodies," *Phys. Z. Sowjetunion*, vol. 8, pp. 153–169, 1935.
- [17] L. Liu, C. F. Pai, Y. Li, H. W. Tseng, D. C. Ralph, and R. A. Buhrman, "Spin-torque switching with the giant spin Hall effect of Tantalum," *Science*, vol. 336, no. 6081, pp. 555–558, May 2012.
- [18] L. Liu, O. J. Lee, T. J. Gudmundsen, D. C. Ralph, and R. A. Buhrman, "Current-induced switching of perpendicularly magnetized magnetic layers using spin torque from the spin Hall effect," *Physical Review Letters*, vol. 109, no. 9, pp. 096 602–1–096 602–5, Aug 2012.
- [19] L. Thomas and S. S. Parkin, "Current induced domain-wall motion in magnetic nanowires," in *Handbook of Magnetism and Advanced Magnetic Materials*. Wiley Online Library, 2007.
- [20] M. Fiebig, "Revival of the magnetoelectric effect," Journal of Physics D: Applied Physics, vol. 38, no. 8, pp. R123–R152, April 2005.
- [21] M. G. Mankalale, Z. Liang, A. K. Smith, D. C. Mahendra, J. P. Wang, and S. S. Sapatnekar, "A fast magnetoelectric device based on current– driven domain wall propagation," in *Proceedings of the 74th IEEE Device Research Conference*, June 2016.
- [22] M. J. Donahue and D. G. Porter, *OOMMF User's Guide*. US Department of Commerce, Technology Administration, National Institute of Standards and Technology, 1999.
 [23] P. P. Horley, A. Sukhov, C. Jia, E. Martínez, and J. Berakdar, "Influence
- [23] P. P. Horley, A. Sukhov, C. Jia, E. Martínez, and J. Berakdar, "Influence of magnetoelectric coupling on electric field induced magnetization reversal in a composite unstrained multiferroic chain," *Physical Review B*, vol. 85, pp. 054401–1–054401–6, Feb 2012.
- [24] R. C. O'Handley, Modern Magnetic Materials: Principles and Applications. Wiley, 1999.
- [25] R. P. Cowburn and M. E. Welland, "Room temperature magnetic quantum cellular automata," *Science*, vol. 287, no. 5457, pp. 1466–1468, Feb 2000.
- [26] S. C. Chang, S. Manipatruni, D. E. Nikonov, and I. A. Young, "Clocked domain wall logic using magnetoelectric effects," *IEEE Journal on Exploratory Solid–State Computational Devices and Circuits*, (to appear; early version available on IEEE Xplore).
- [27] S. Datta, S. Salahuddin, and B. Behin-Aein, "Non-volatile spin switch for Boolean and non-Boolean logic," *Applied Physics Letters*, vol. 101, no. 25, pp. 252411–1–252411–5, Dec 2012.
- [28] S. Emori, U. Bauer, S. M. Ahn, E. Martinez, and G. S. D. Beach, "Current–driven dynamics of chiral ferromagnetic domain walls," *Nature Materials*, vol. 12, no. 7, pp. 611–616, June 2013.
- [29] S. Fukami, T. Suzuki, N. Ohshima, K. Nagahara, and N. Ishiwata, "Micromagnetic analysis of current driven domain wall motion in nanostrips with perpendicular magnetic anisotropy," *Journal of Applied Physics*, vol. 103, no. 7, pp. 07E718–1–07E718–4, Jan 2008.
- [30] S. H. Yang, K. S. Ryu, and S. S. Parkin, "Domain-wall velocities of up to 750 ms⁻¹ driven by exchange-coupling torque in synthetic antiferromagnets," *Nature Nanotechnology*, vol. 10, no. 3, pp. 221–226, Feb 2015.
- [31] S. Manipatruni, D. E. Nikonov, and I. A. Young, "Spin-orbit logic with magnetoelectric nodes: A scalable charge mediated nonvolatile spintronic logic," 2015, available at https://arxiv.org/abs/1512.05428.
- [32] S. Mizukami, T. Kubota, X. Zhang, H. Naganuma, M. Oogane, Y. Ando, and T. Miyazaki, "Influence of Pt doping on Gilbert damping in permalloy films and comparison with the perpendicularly magnetized alloy films," *Japanese Journal of Applied Physics*, vol. 50, no. 10R, pp. 103 003–1–103 003–5, Oct 2011.
- [33] T. A. Moore, I. M. Miron, G. Gaudin, G. Serret, S. Auffret, B. Rodmacq, A. Schuhl, S. Pizzini, J. Vogel, and M. Bonfim, "High domain wall velocities induced by current in ultrathin Pt/Co/AlOx wires with perpendicular magnetic anisotropy," *Applied Physics Letters*, vol. 93, no. 26, pp. 262 504–1–262 504–4, Dec 2008.
- [34] T. L. Gilbert, "A phenomenological theory of damping in ferromagnetic materials," *IEEE Transactions on Magnetics*, vol. 40, no. 6, pp. 3443– 3449, Nov 2004.
- [35] W. Bailey, P. Kabos, F. Mancoff, and S. Russek, "Control of magnetization dynamics in Ni₈₁Fe₁₉ thin films through the use of rare-earth dopants," *IEEE Transactions on Magnetics*, vol. 37, no. 4, pp. 1749– 1754, July 2001.
- [36] X. Yao, J. Harms, A. Lyle, F. Ebrahimi, Y. Zhang, and J. P. Wang, "Magnetic tunnel junction-based spintronic logic units operated by spin transfer torque," *IEEE Transactions on Nanotechnology*, vol. 11, no. 1, pp. 120–126, Jan 2012.