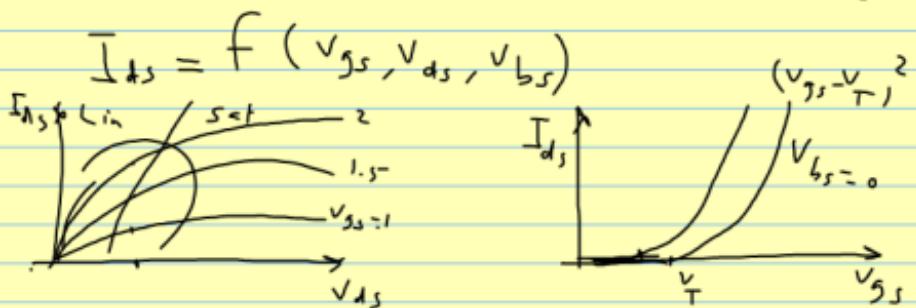
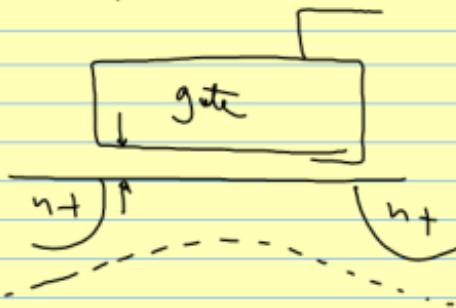
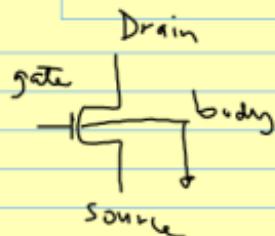
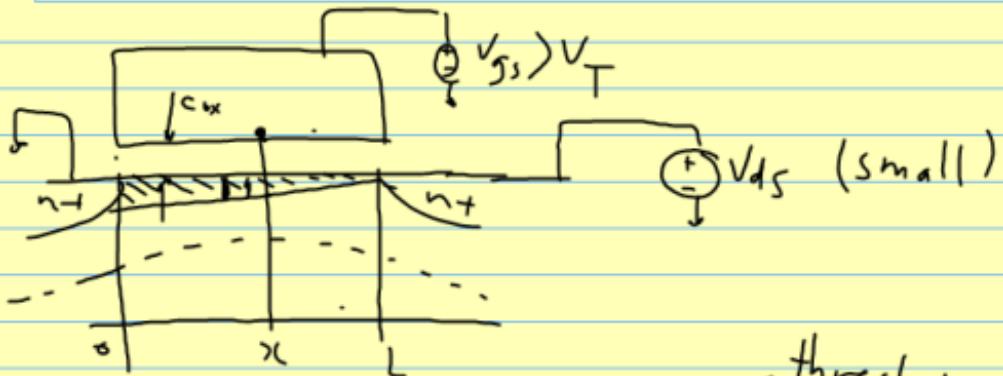


MOS Tr



Triode (Linear) region



$$Q(x) = -C_{ox} (V_{gs} - V(x) - V_t)$$

$\downarrow \frac{\epsilon_{ox}}{t_{ox}}$

threshold Volts

$$I_{ds} = -C_{ox} (V_{gs} - V(x) - V_t) \cdot V_n(x) \cdot W$$

$-M_c E(x)$
 $-\mu \frac{dV(x)}{dx}$

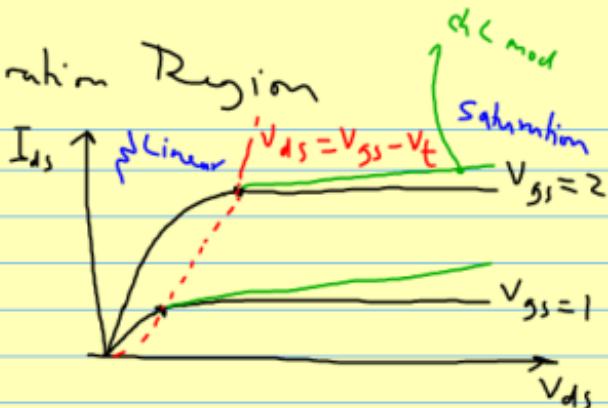
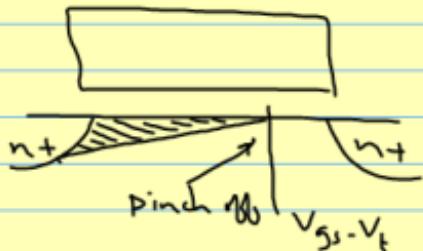
Simple Model Linear Region

$$\int_{-L}^L I_{ds} \cdot dx = \int_{-L}^{V_{ds}} \mu_c C_{ox} (V_{gs} - V_t - V(x)) \cdot W \cdot dV(x)$$

$$I_{ds} = \mu c_{ox} \frac{W}{L} \underbrace{\left((V_{gs} - V_t) \cdot V_{ds} - \frac{1}{2} V_{ds}^2 \right)}_{\approx \frac{1}{R_{ch}}}$$

what if $V_{ds} > V_{gs} - V_t$

Saturation Region



$$I_{ds} \Big|_{V_{ds}=V_{GS}-V_t} = \mu C_{ox} \frac{w}{L} ((V_{GS}-V_t) \cdot (V_{GS}-V_t) - \frac{1}{2} (V_{GS}-V_t)^2)$$

$$I_{ds} \Big|_{V_{ds}=V_{GS}-V_t} = \mu C_{ox} \frac{w}{2L} (V_{GS}-V_t)^2$$

Channel Modulation

$$L_{\text{real}} = L - \zeta \cdot V_{ds}$$

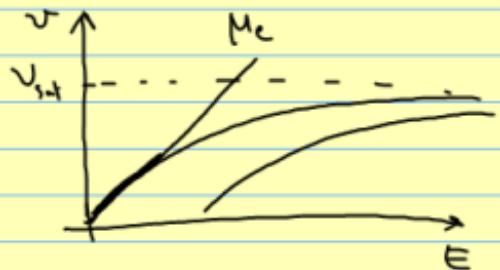
$$I_{ds} = \mu C_{ox} \frac{w}{2(L - \zeta V_{ds})} \cdot (V_{gs} - V_t)^2$$

$$= \mu C_{ox} \frac{w}{2L} (V_{gs} - V_t)^2 \cdot \frac{1}{1 - \zeta \frac{V_{ds}}{L}}$$

$(1 + \frac{\zeta}{L} V_{ds})$

ch length
modulation

Velocity Sat



$$v = \begin{cases} v_{sat} & E > E_c \\ \frac{M_e E}{(1 + (\frac{E}{E_c})^n)^{\frac{1}{n}}} & E < E_c \end{cases}$$

$n=1$ for PMOS
 $n=2$ for NMOS

$$I_{ds} = C_{ox} (V_{ss} - V_t - V(x)) \cdot \frac{M_e \frac{dV(x)}{dx}}{1 + \frac{dV_x}{dx} \cdot \frac{1}{E_c}} \cdot W$$

$$\int_0^L () dx = \int_0^{V_{ds}} () dV(x)$$

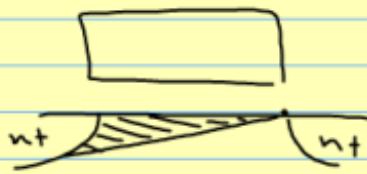
Velocity Sat E_f

$$I_{ds} = \mu_e C_{ox} \frac{w}{L} ((v_{gs} - v_t) \cdot v_{ds} - \frac{1}{2} v_{ds}^2)$$

$$\frac{1}{1 + \frac{v_{ds}}{E_c \cdot L}}$$

extreme cases $L \rightarrow \infty$ $K(v_{ds}) \Rightarrow 1$ Long ch $K(v_{ds}) < 1$ Short ch





$$V_{ds} = V_{dsat}$$

$$v = v_{sat}$$

$$E = E_c$$

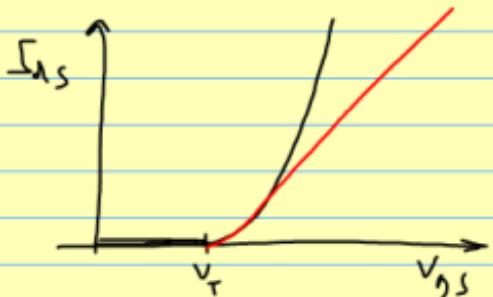
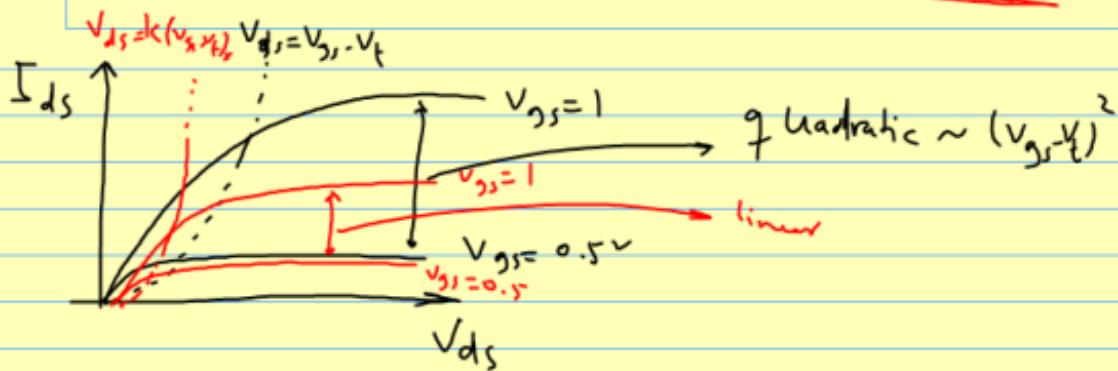
$$I_{ds} = C_{ox} (V_{gs} - V_t - V_{dsat}) \cdot v_{sat} \cdot w \quad \otimes$$

$$I_{ds} \otimes = I_{ds} \otimes \otimes \otimes \text{ Solve for } V_{dsat}$$

$$\underline{V_{dsat}} = \frac{1}{1 + \frac{V_{gs} - V_t}{E_c L}} \cdot (V_{gs} - V_t)$$

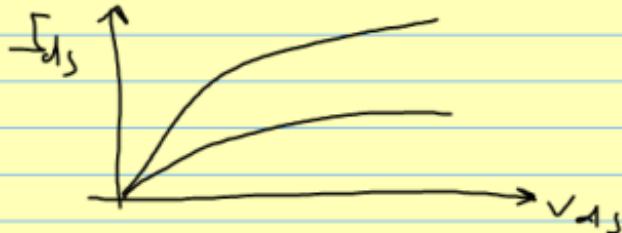
$$= K \underline{(V_{gs} - V_t)} \cdot (V_{gs} - V_t)$$

Long ch vs Short-ch



Velocity Saturation

- 1- The current prematurely saturates in short ch device.
(extended Sds region)
2. I_{dsat} linear relationship V_{gs}
3. I_{dsat} has a weaker L dependence

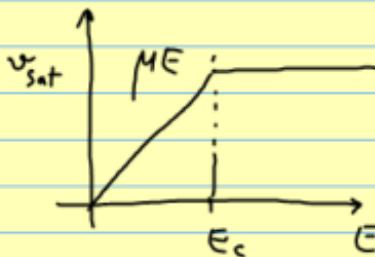


Simplified Velocity Sat Model

(i) assumption I

$$v = \begin{cases} v_{\text{sat}} & E > E_c \\ \mu E & E < E_c \end{cases}$$

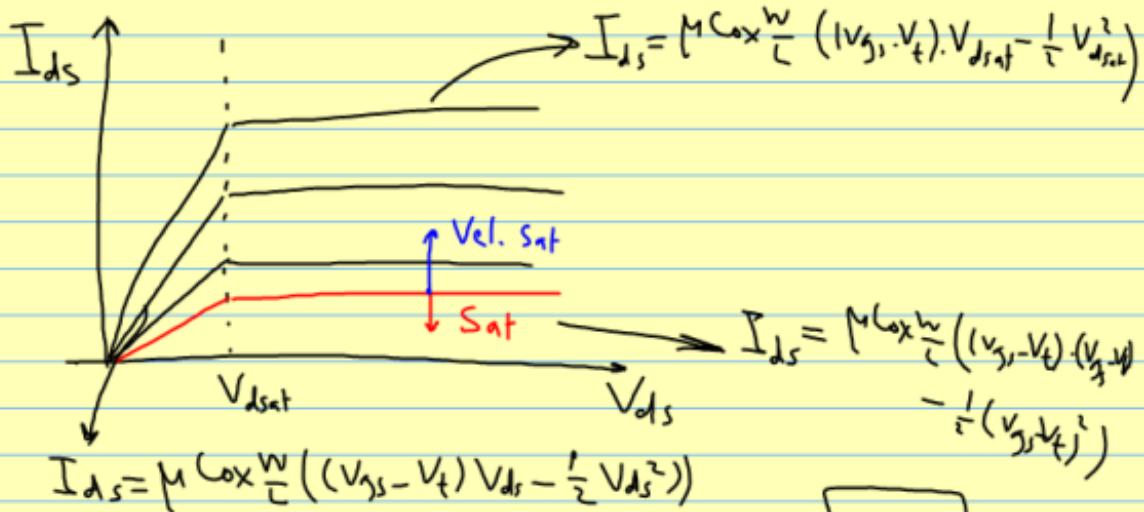
$$v_{\text{sat}} = \mu E_c$$



(ii) Assumption II for large $(V_{gs} - V_t)$

$$\underline{v_{dsat}} = \text{const} \approx E_c L = L \frac{v_{\text{sat}}}{\mu}$$

Unified MOS Model

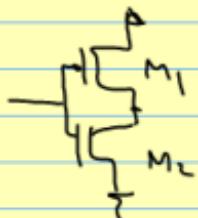


Unified Model

$$I_{ds} = \begin{cases} 0 & V_{gs} < V_t \\ \mu C_{ox} \frac{w}{L} \left((V_{gs} - V_t) V_{min} - \frac{1}{2} V_{min}^2 \right) \times (1 + \lambda V_{ds}) & V_{gs} > V_t \end{cases}$$

$$V_{min} = \min(V_{gs} - V_t, V_{ds}, V_{dsat})$$

CAD Tools



↓
extract
netlist

M1 w= L=

M2 w= L=

wire ---

↓
Run HSpice