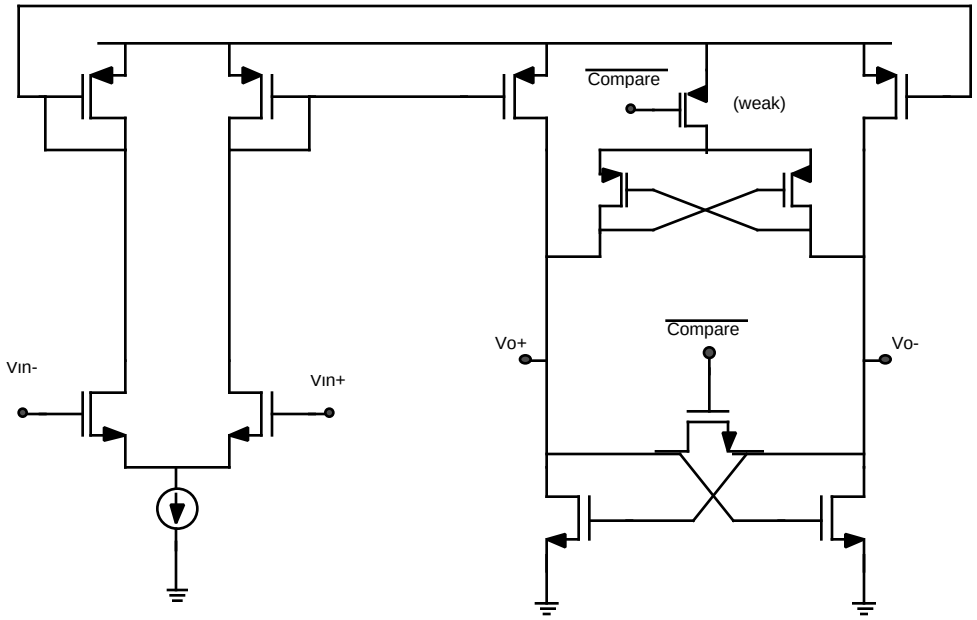
EE8331: Advanced Analog Integrated Circuit Design

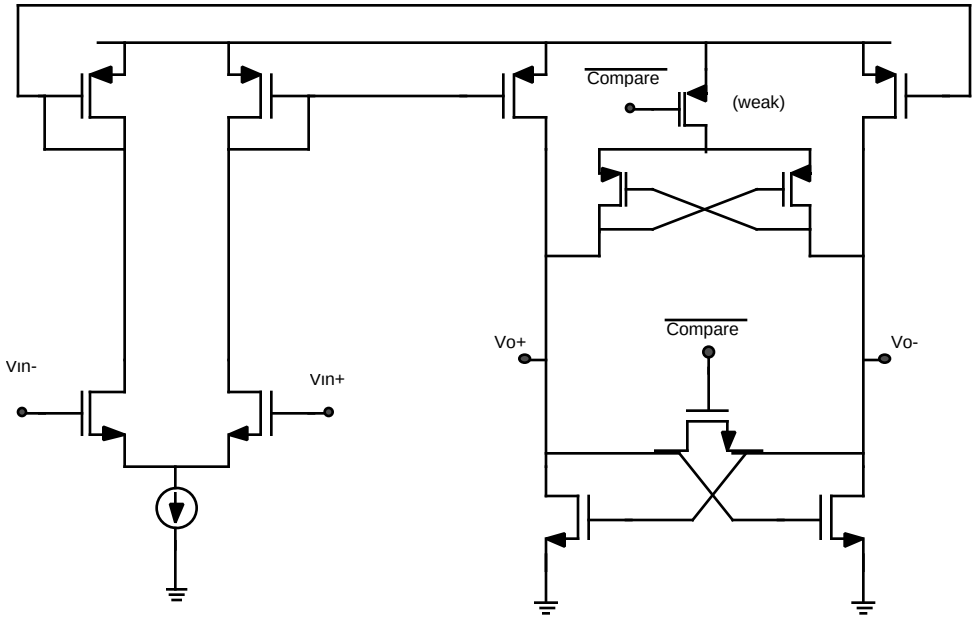
HW #3
Due Monday 04/17/00

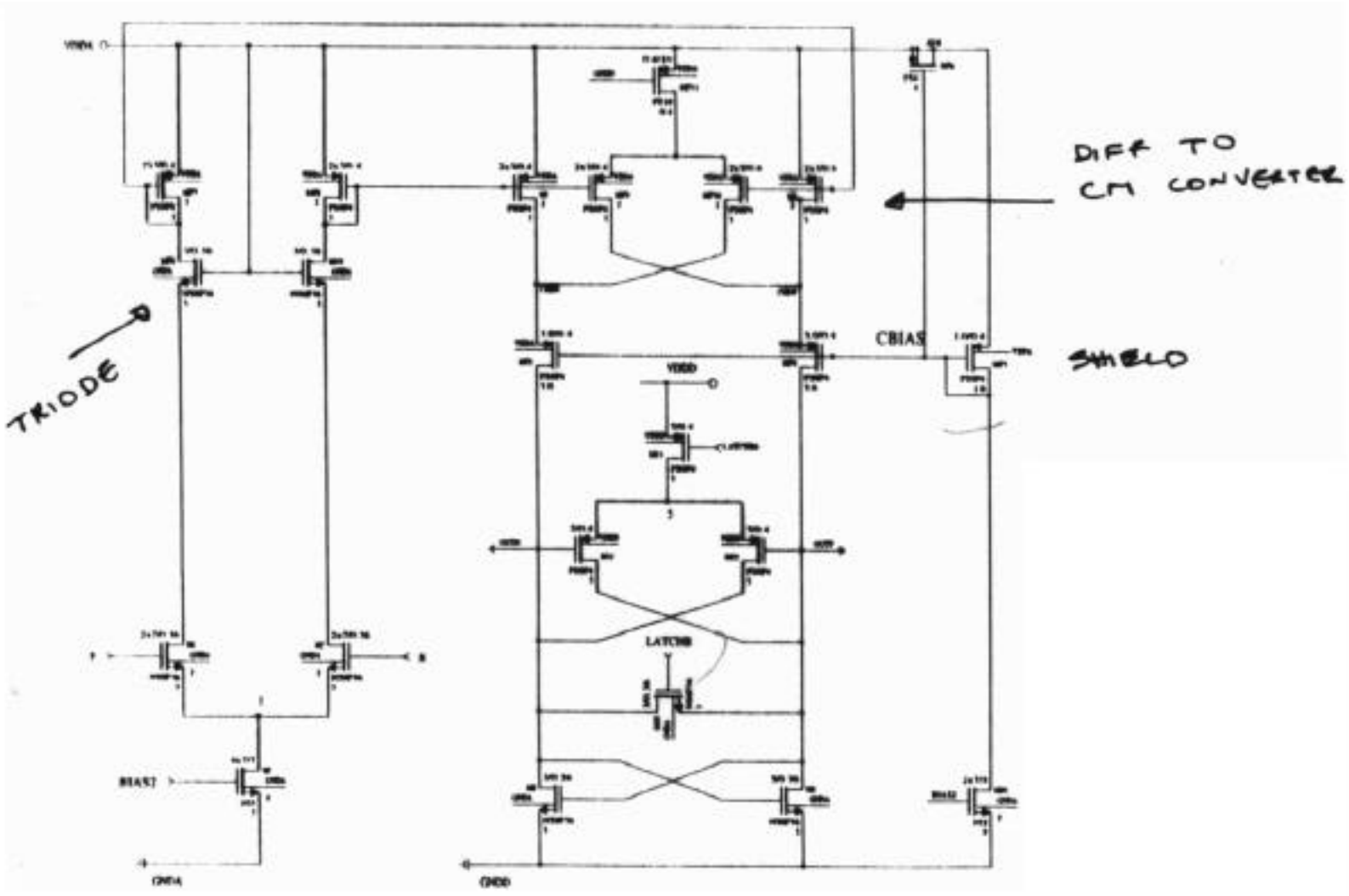
1) Develop an autozero scheme for the comparator shown below. Illustrate the result with a 10 mV input referred offset error and correcting it. Use simulation (SPICE) to illustrate this answer.

Comparator without flashback compensation or pull up scheme



Comparator with pull-up scheme but no flashback compensaton




DIFF TO
CM CONVERTER
TRIODE
CBIAS
SHIELD
VDDD
LATCHB
BIAS2
GNDA
GNDD