

# Clustering Based Pruning for Statistical Criticality Computation under Process Variations

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**Abstract**— We present a new linear time technique to compute criticality information in a timing graph by dividing it into “zones”. Errors in using tightness probabilities for criticality computation are dealt with using a new clustering based pruning algorithm which greatly reduces the size of circuit-level cutsets. Our clustering algorithm gives a 150X speedup compared to a pairwise pruning strategy in addition to ordering edges in a cutset to reduce errors due to Clark’s *MAX* formulation. The clustering based pruning strategy coupled with a localized sampling technique reduces errors to within 5% of Monte Carlo simulations with large speedups in runtime.

## I. INTRODUCTION AND PREVIOUS WORK

With scaling of technology, process parameter variations render the circuit delay as unpredictable [6], making sign-off ineffective in assuring against chip failure. Recent works concerning Statistical Static Timing Analysis (SSTA) in [1], [9] deal with this issue by treating the delay of gates and interconnects as random variables with Gaussian distributions, and predict the mean and variance of circuit delay to within a few percents.

With process variations in effect, no one path dominates the delay of the circuit [5]. Work in [9] proposes the concept of edge(node) criticality, which is the probability that an edge(node) lies on the critical path in a manufactured chip. Works like [2], [5], [10] compute the criticality probability of edges in a timing graph, using a canonical first order delay model.

In [9], the authors perform a reverse traversal of the timing graph assuming independence of edge criticalities despite structural and spatial correlations in the circuit. Subsequently, the work in [5] used sensitivity matrices to compute the criticalities of nodes and edges in the timing graph, with complexity potentially cubic in the number of principal components of the circuit due to the matrix multiplications involved. Work in [2] perturbs gate delays to compute its effect on the circuit output delay, using cutsets to simplify computation. The quadratic time complexity of this approach is reduced to linear in [10], where the criticality of edges in a cutset is computed using a balanced binary partition tree.

Our contributions are twofold. We propose a linear time algorithm to compute the criticality probability of edges (nodes) in a timing graph using the notion of cutsets. Edges crossing multiple cutsets are dealt with using a novel zone based approach, similar to [11], where old computations are reused to the greatest possible extent. We develop a clustering based pruning algorithm to effectively eliminate a large number of non-competing edges in cutsets of several thousand edges. The proposed clustering scheme helps order the *MAX* operations in a set, a source of significant error as shown in [8]. Localized sampling on the pruned cutset further reduces errors in edge criticalities to within 5% of Monte Carlo simulations, with large speedups in run-time when compared to a pairwise pruning strategy.

## II. BACKGROUND

### A. Correlation Model and SSTA

We use the spatial correlation model in [1] to model intra-die parameter correlations. Briefly, the chip is divided into a uniform grid in which gates in a grid square have perfect correlation and gates in far-away grid squares have weak correlations. A covariance matrix of size equal to the number of grid squares is obtained for each modeled parameter. We model the length and width of gates, and the thickness, width and inter-layer dielectric thickness for interconnects. Process parameters are assumed Gaussian as in [1], [9] and using the principal component analysis (PCA) technique each correlated parameter is expressed as a sum of independent normal random variables, or principal components (PCs). Delays of edges in the timing graph are expressed in terms of the PCs and SSTA is then performed by a forward propagation on the timing graph. For more details, readers are referred to [1].

### B. Definitions

**Definition II.1 (Timing Graph).** A timing graph  $G(V, E)$  of a circuit is a directed acyclic graph with  $V$  nodes representing gate terminals and  $E$  edges representing connections between them. Primary inputs and outputs are connected, respectively, to a virtual source node,  $v_s$ , and a virtual sink node,  $v_t$ . The delay of an edge in  $G$  is represented by a probability density function (*pdf*).

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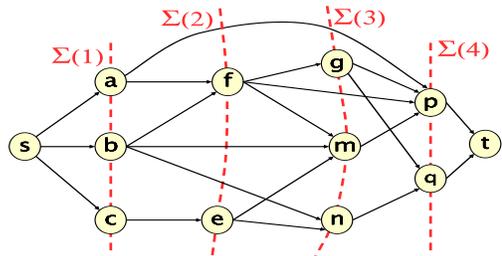


Fig. 1. Example timing graph  $G$  (depth  $L$ ) illustrating cutsets  $\Sigma(1) - \Sigma(4)$ .

**Definition II.2 (Cutset).** A cutset  $\Sigma$  is a set of edges/nodes in  $G$  such that every  $v_s$  to  $v_t$  path passes through one and only one member of  $\Sigma$ .

**Definition II.3 (Arrival Time (Required Time)).** The arrival time  $AT$  (required time  $RT$ ) at an edge/node in  $G$  is the maximum delay from any primary input (output) to the edge/node. Like delays, these are also *pdfs*.

**Definition II.4 (Path Delay).** The path delay of an edge/node (hereon, also referred to as edge delay)  $i$  in  $G$  is defined as  $e_i = AT_i + RT_i$  and is a *pdf*. Each path delay  $e_i$  is represented in a canonical form in terms of independent principal components (PCs)  $p_j$ , as

$$e_i = \mu_i + \sum_{j=1}^{j=k} a_{ij} \cdot p_j \quad (1)$$

Here  $a_{ij}$  is the coefficient of PC  $j$  for edge  $i$  and  $k$  is the total number of PCs. The mean of the edge delay is given by  $\mu_i$ .

**Definition II.5 (Complementary Path Delay).** Given a cutset,  $\Sigma$ , in a timing graph  $G$ , the complementary path delay of an edge/node  $k \in \Sigma \subset G$ ,  $e_k^c$ , is defined as the *pdf* of  $MAX(\forall e_i : e_i \in \Sigma, e_i \neq e_k)$ .

**Definition II.6 (Local Criticality).** The local criticality (also referred to as tightness probability [9])  $\tau_{ij}$  of edge  $e_i$  with respect to  $e_j$ , with means  $\mu_i, \mu_j$  and standard deviations  $\sigma_i, \sigma_j$  respectively, and correlation  $\rho_{ij}$ , is given by

$$\tau_{ij} = \Phi\left(\frac{\mu_i - \mu_j}{\theta}\right) \quad (2)$$

where  $\theta = \sqrt{\sigma_i^2 + \sigma_j^2 - 2 \cdot \rho_{ij} \cdot \sigma_i \cdot \sigma_j}$ . The **degree of domination** of edge  $i$  over  $j$  is represented by  $\tau_{ij}$ .  $\Phi$  is the distribution (*cdf*) of a unit normal random variable  $\mathcal{N}(0, 1)$ . It can be shown that  $\tau_{ji} = 1 - \tau_{ij}$ .

**Definition II.7 (Global Criticality).** The global criticality  $T_i$  (also referred to as criticality hereon) of edge  $e_i$  in cutset  $\Sigma$  is the probability that it has maximum delay among all the edges in the cutset, i.e.,

$$= \Pr(e_i \geq e_i') \quad (\text{see Def. II.5}) \quad (3)$$

$T_i$  is also referred to as the **criticality probability** of  $e_i$ . It follows that the global criticality of an edge  $e_i$  in  $\Sigma$  cannot be greater than its local criticality with respect to any other edge in  $\Sigma$ , i.e.,

$$T_i \leq \tau_{ij} \quad \{ \forall e_j \in \Sigma, e_j \neq e_i \} \quad (4)$$

**Definition II.8 ( $MAX_\theta$ ).** The maximum  $MAX_\theta$  of two normal random variables  $e_i$  and  $e_j$  in canonical form using Clark’s formulation [3] is given by  $c = MAX_\theta(i, j)$ , where  $\phi$  is the density (*pdf*) function of  $\mathcal{N}(0, 1)$  and

$$\begin{aligned} \mu_c &= \tau_{ij} \cdot \mu_i + \tau_{ji} \cdot \mu_j + \theta \cdot \phi\left(\frac{\mu_i - \mu_j}{\theta}\right) \\ \sigma_c^2 &= \tau_{ij} \cdot (\sigma_i^2 + \mu_i^2) + \tau_{ji} \cdot (\sigma_j^2 + \mu_j^2) \\ &\quad + (\mu_i + \mu_j) \cdot \theta \cdot \phi\left(\frac{\mu_i - \mu_j}{\theta}\right) - \mu_c^2 \\ c_k &= \tau_{ij} \cdot a_{ik} + \tau_{ji} \cdot a_{jk} \end{aligned} \quad (5)$$

## III. STATISTICAL CRITICALITY COMPUTATION

This section describes a quadratic time complexity statistical criticality (SC) algorithm followed by details of our linear time approach.



Observations 1 and 2 are consistent with Eq. 4. Now consider global criticality of  $b$ , using the cutset approach. We first compute complementary path delay  $b' = MAX(a, c)$ . It follows (Def. II.7) that  $T_b = \Pr(b \geq MAX(a, c))$ . With Clark's formulation  $MAX_\theta$ , for the  $MAX$ , we get  $T_b = \Pr(b \geq MAX_\theta(a, c)) = 0.297$ .

Intuitively, for this scenario Clark's formulation is accurate with respect to local criticality  $t_{ba}$  of  $b$ , but it overestimates global criticality  $T_b$ , and is inconsistent with Eq. 4.

**Definition IV.1 (Local Errors).** With respect to Clark's formulation, edge  $e_i$  in cutset  $\Sigma$  is said to have **local errors** iff there exists some edge  $e_j \in \Sigma$  for which its local criticality is smaller than its global criticality, i.e.,

$$\{ \exists e_j \in \Sigma, e_j \neq e_i \} : \tau_{ij} < T_i \quad (10)$$

In other words, Eq. 4 does not hold. The difference,  $T_i - \tau_{ij}$  is called the **local error** of  $e_i$ . By definition, local errors always overestimate the criticality of an edge in  $\Sigma$ . In our toy example,  $b$  exhibits local errors with a magnitude of 0.297. Local errors were found to propagate in the **ZSC** algorithm, where variables (edges) like  $b$  that should never have been critical, were found to have a significant global criticality.

To further see the inconsistencies of the tightness probability approach using Clark's formulation we consider the event  $a \geq MAX_\theta(b, c)$ , with  $a$  represented as  $a = a_0 + \sum a_i p_i = a_0 + a_p$ . Variables  $b$  and  $c$  are represented similarly. We have,

$$\begin{aligned} \text{Event } \{a \geq MAX_\theta(b, c)\} &\equiv \\ \tau_{bc}(a - b) + \tau_{cb}(a - c) &\geq \alpha + s(\tau_{bc}b_p + \tau_{cb}c_p) \end{aligned} \quad (11)$$

Here  $\alpha$  and  $s$  are factors used to equate the first two moments (mean and standard deviation) of the non-Gaussian  $MAX$  with Clark's Gaussian  $MAX_\theta$  approximation. Values of  $\alpha \approx 0$  and  $s \approx 0$  could result in a true event even for cases in which  $b \leq a \leq c$  or  $c \leq a \leq b$ , overestimating the global criticality  $T_a$  of  $a$ . Values of  $\alpha > 0$  could result in a false event even in the case that  $a \geq b$  and  $a \geq c$ , underestimating  $T_a$ .

**Definition IV.2 (Global Errors).** With respect to Clark's formulation, edge  $e_i$  in a cutset  $\Sigma$  is said to have **global errors**, iff its computed criticality  $T_i$  differs from its true criticality and the edge does not exhibit local errors, i.e.,

$$T_i \leq \tau_{ij} \quad \{ \forall e_j \in \Sigma, e_j \neq e_i \} \quad (12)$$

and  $T_i$  is in error.

By definition, global errors can either overestimate or underestimate the criticality. For the toy example,  $T_a$  is underestimated by 0.567. Note that the value of  $T_a$  is consistent with Eq. 4, since both  $\tau_{ab} = 1.0$  and  $\tau_{ac} = 0.921$  are greater than  $T_a = 0.356$

In summary, although local and global errors result from the linear approximation of Clark's  $MAX_\theta$  operation, local errors are an artifact of the manner in which we compute global criticalities of edges in a cutset whereas global errors are more fundamental, overestimating or underestimating the true criticality of an edge in a cutset.

## V. CLUSTERING BASED PRUNING AND ORDERING

**Definition V.1 (Dominant and Non-dominant Edges).** An edge  $e_i$  in set  $\Sigma$  is said to be **dominant** iff its local criticality with respect to all other edges in  $\Sigma$  is above a threshold  $\varepsilon$ , i.e.,

$$\tau_{ij} > \varepsilon \quad \{ \forall e_j \in \Sigma, e_j \neq e_i \} \quad (13)$$

Otherwise,  $e_i$  is said to be **non-dominant** in  $\Sigma$ , i.e.,

$$\{ \exists e_j \in \Sigma, e_j \neq e_i \} : \tau_{ij} \leq \varepsilon \quad (14)$$

**Definition V.2 (Mutually-dominant Edges).** A set of edges  $\Sigma$  are said to be **mutually dominant** iff each edge in  $\Sigma$  is dominant, i.e.,

$$\tau_{ij} > \varepsilon \quad \{ \forall e_i, e_j \in \Sigma, e_j \neq e_i \} \quad (15)$$

As seen in the previous section, non-dominant edges (like  $b$  in Fig. 2) in a cutset exhibit local errors. Moreover, they also contribute to global errors of other edges in the cutset (like  $a$  in Fig. 2). To avoid the bulk of these errors we propose to prune the cutset, eliminating its non-dominant edges from competing and injecting errors in global criticality computations.

Pruning is justified by Eq. 4, wherein eliminating edge  $e_i$  with local criticality lower than a sufficiently small threshold value  $\varepsilon$  does not hurt global criticality computations because  $T_i \leq \varepsilon$ . The benefits are accentuated in cutsets with dominant edges with large global criticalities, since the sum of global criticalities across a cutset equals 1.0 (implying that many edges have very small local criticalities).

It must be pointed out, that not every edge with global criticality below  $\varepsilon$  can be eliminated by pruning because its local criticality might not be smaller than  $\varepsilon$ . Such edges cause global errors using the linear  $MAX_\theta$  operation.

### A. nC2 Cutset Pruning

A straightforward approach to pruning a cutset would be to perform pairwise comparisons of edges, eliminating those that have a minimal local criticality less than a predefined threshold  $\varepsilon$ . The main drawback of this approach is its quadratic time complexity of  $O(n^2)$ , due to  $nC2$  local criticality computations, where  $n$  is the number of edges in the cutset. This can be prohibitive for large circuits.

### B. Clustering Based Cutset Pruning and Ordering

To overcome the quadratic runtime complexity overhead of the  $nC2$  approach we present a new clustering-based pruning heuristic based on the  $K$ -center clustering algorithm of [4]. Algorithm 3 describes the procedure. The basic idea is to prune a cutset into a set of mutually dominant edges by removing its non-dominant edges. Variables used in the algorithm are,

$\kappa$ : Each cluster $\sigma$ contains a center $\kappa$ $d_{i\kappa}$ : Distance of an object $i$ from its cluster center $\kappa$ is its local criticality $\tau_{i\kappa}$ $r_\sigma$ : Radius of cluster $\sigma$ is distance of the object farthest from $\kappa$ , i.e., $r_\sigma = MAX(d_{i\kappa}) \forall i \in \sigma$ $R_\sigma$ : Border $R$ of cluster $\sigma$ is object with the maximum distance from $\kappa$ , i.e., $R_\sigma = j : d_{j\kappa} = r_\sigma$
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We first prune cutset  $\Sigma$  with respect to seed  $\chi$ , chosen as the object with maximum mean in  $\Sigma$ . Steps 5-8 of Algorithm 3 compute new clusters from existing ones (Algorithm 4) until no cluster has size exceeding  $S$ . Finally, cutset  $\Sigma$  is replaced by the remaining un-pruned objects.

**Algorithm 3**  $K = KCenterPrune(\Sigma, \varepsilon, S) // \Sigma = \text{cutset}; \varepsilon = \text{pruning threshold}; S = \text{max. cluster size}; K = \# \text{ clusters}$

- 1:  $\Omega = NULL; \sigma = NULL; K = 0$  {  $\Omega$  is the set of clusters,  $\sigma$  is the 1<sup>st</sup> cluster }
- 2: Assign seed  $\chi = \text{object with max. } \mu \in \Sigma$  as the center of  $\sigma$
- 3: Prune  $\Sigma$  with respect to  $\chi$ ; mark  $\chi = \text{pruned}$  if  $\exists j \in \Sigma : (1 - \tau_{j\chi}) \leq \varepsilon$
- 4: Compute  $r_\sigma$  and  $R_\sigma$ ;  $K = K + 1$ ;  $\Omega(K) = \sigma$  {insert cluster  $\sigma$  into  $\Omega$ }
- 5: **while** (max. size of a cluster in  $\Omega > S$ ) **do**
- 6:      $\delta = \text{CreateNewCluster}(\Omega)$
- 7:      $K = K + 1$ ;  $\Sigma(K) = \delta$  {insert new cluster  $\delta$  into  $\Omega$ }
- 8: **end while**
- 9: insert all un-pruned objects of  $\Omega$  in  $\Sigma$  and return  $K$

Intuitively,  $\chi$  in Algorithm 4 is the object upon which its center has the lowest degree of domination (Def II.6) and hence a good candidate to facilitate pruning of other edges in the cutset. Step 3 uses  $\chi$  to prune objects  $j$  (with local criticality with respect to  $\chi$  less than  $\varepsilon$ ) from their respective clusters. Alternatively, if  $\chi$  has a higher degree of domination over  $j$  compared to its current center  $\kappa$ ,  $j$  is removed from its cluster and inserted into new cluster  $\delta$  (Step 3). Intuitively, a greater degree of domination between two edges results in smaller global errors in  $MAX_\theta$ . We return  $\delta$  after adjusting its radius and border and those of all currently existing clusters in  $\Omega$  (Step 4). The algorithm has the following properties.

**Algorithm 4**  $\delta = \text{CreateNewCluster}(\Omega) // \Omega = \text{set of clusters}; \delta = \text{new cluster}$

- 1: Assign  $\chi = \text{border of cluster with max. radius as center } \kappa$  of new cluster  $\delta$
- 2: Prune  $\Omega$  with respect to  $\chi$ ; mark  $\chi = \text{pruned}$  if  $\exists j \in \Omega : (1 - \tau_{j\chi}) \leq \varepsilon$
- 3: if  $\exists j \in \Omega : \tau_{j\chi} < d_{j\kappa}$ , remove  $j$  from its current cluster and insert into  $\delta$
- 4: compute radius and border for  $\delta$  and all existing clusters in  $\Omega$ ; return  $\delta$

**Property V.1.** At any iteration, all objects in  $\Omega$  (excluding cluster centers marked pruned) are dominant with respect to all existing cluster centers.

*Proof:* To avoid pruning in Line 3 of Algorithm 3, objects must be dominant with respect to the seed (1<sup>st</sup> cluster center). Moreover, every object  $j$  is compared with every newly added cluster center (over the entire run of the algorithm, all cluster centers) in Line 2 of Algorithm 4. Clearly,  $j$  must be dominant with respect to these centers to avoid being pruned. Moreover, Line 3 of Algorithm 3 and Line 2 of Algorithm 4 compare every cluster center with every object for dominance. Although not removed from  $\Omega$ , centers are marked pruned if they are non-dominant with respect to other cluster objects. ■

**Property V.2.** With  $S = 1$ ,  $KCenterPrune(\Sigma, \varepsilon, 1)$  returns a set of mutually dominant edges (see Def.V.2) in  $\Sigma$ .

*Proof:* With  $S = 1$  each cluster in  $\Omega$  contains only one object, its cluster center. From Property V.1 above we know that that these are either marked pruned or are dominant with respect to other cluster centers. It follows from step 9 of Algorithm 3 that  $\Sigma$  contains mutually dominant objects. ■

**Property V.3.** For any cluster  $\sigma \in \Omega$ , its center  $\chi$  has a **higher degree of domination** over its members than any other cluster center  $\kappa$ , i.e.,

$$\tau_{\chi j} > \tau_{\kappa j} \quad \{ \forall j \in \sigma, \kappa \in \Sigma, \kappa \neq \chi \} \quad (16)$$

TABLE II  
CRITICALITY RUN-TIMES AND ERRORS FOR VARIOUS BENCHMARKS;  $\varepsilon = 5\%$  AND  $N_{Is} = 1000$

Metric	Pruning Scheme	Benchmark								
		s27	s1196	s5378	s9234	s13207	s15850	s38417	s35932	s38584
max. $\delta\%$	ZSC	<b>3.84</b>	<b>2.25</b>	<b>47.99</b>	<b>45.79</b>	<b>38.11</b>	<b>58.49</b>	<b>40.82</b>	<b>24.99</b>	<b>43.56</b>
	nC2	3.84	2.99	3.13	21.97	2.65	6.43	37.82	16.68	18.65
	CPSC	3.84	2.99	3.13	23.65	5.30	5.49	37.82	16.12	18.65
	CPSC+TGR+LS	<b>3.84</b>	<b>2.99</b>	<b>3.13</b>	<b>2.92</b>	<b>5.21</b>	<b>2.37</b>	<b>2.17</b>	<b>2.79</b>	<b>15.82</b>
run time (sec)	ZSC	<b>0.00</b>	<b>0.03</b>	<b>0.16</b>	<b>0.30</b>	<b>0.45</b>	<b>0.54</b>	<b>2.51</b>	<b>2.71</b>	<b>3.06</b>
	nC2	0.00	0.02	0.39	0.71	2.31	2.19	43.53	55.39	51.76
	CPSC	0.00	0.01	0.02	0.05	0.07	0.09	0.27	0.34	0.25
	CPSC+TGR+LS	<b>0.00</b>	<b>0.00</b>	<b>0.03</b>	<b>0.08</b>	<b>0.09</b>	<b>0.11</b>	<b>0.34</b>	<b>0.37</b>	<b>0.34</b>
$\eta$	ZSC	7	202	593	644	1329	1688	2821	7340	5680
	nC2	<b>2</b>	<b>1</b>	<b>2</b>	<b>11</b>	<b>4</b>	<b>4</b>	<b>5</b>	<b>49</b>	<b>11</b>
	CPSC	2	1	2	11	4	4	5	49	11
	CPSC+TGR+LS	<b>2</b>	<b>1</b>	<b>2</b>	<b>11</b>	<b>4</b>	<b>4</b>	<b>5</b>	<b>49</b>	<b>11</b>

*Proof:* This is evident from Step 3 of Algorithm 4. Each object in  $\Omega$  is compared with the new cluster center  $\chi$ . The condition  $\tau_{j\chi} < d_{j\kappa}$  is equivalent to  $\tau_{\chi j} > \tau_{\kappa j}$ , i.e., the new cluster center  $\chi$  has a higher degree of domination over object  $j$  than its current cluster center  $\kappa$ . ■

**Property V.4.** For a cutset  $\Sigma$  of size  $n$  and  $K$  clusters returned, KCenterPrune takes  $O(nK)$  time.

*Proof:* A single run of Algorithm 4 compares every object in  $\Sigma$  with center  $\chi$  of the new cluster  $\delta$ , taking  $O(n)$  time. Since each iteration in Algorithm 3 returns a new cluster, with  $K$  clusters returned, the overall runtime is  $O(nK)$ . ■

### C. CPSC: Clustering Based SC Algorithm

Using Algorithm 3 for SC computation is straightforward. Step 2 in Algorithm 5 derives a set of mutually dominant edges from cutset  $\Sigma$ , facilitated using Property V.2 and orders  $\Sigma$ , facilitated by Property V.3. Such an ordering cannot be obtained with the nC2 pruning strategy of Section V-A. Property V.4 ensures that with a small number of dominant edges  $K$  in a cutset, the algorithm runs in linear time.

### D. Localized Sampling and Timing Graph Reduction

To tackle edges that lead to global errors (Def. IV.2), we perform a quick localized Monte Carlo sampling of the edges in cutset  $\Sigma$  after pruning. We generate  $N_{Is}$  samples of the  $k$  independent identically distributed Gaussians,  $k$  being the number of PCs (Def. II.4). Each sample is used in instantiating  $e_i$  ( $\forall e_i \in \Sigma$ ), from which we compute the edge in  $\Sigma$  with the maximum delay. By keeping count of the number of samples for which an edge in  $\Sigma$  has maximum delay, we can compute its global criticality probability.

#### Algorithm 5 CPSC ( $G(V, E), \varepsilon$ )

- 1: Algorithm 2 Steps 1-7
- 2:  $K = \text{KCenterPrune}(\Sigma, \varepsilon, 1)$ ;  $\text{KCenterPrune}(\Sigma, \varepsilon, S)$
- 3: Algorithm 2 Step 8

The runtime of LS increases with the number of samples  $N_{Is}$  and the depth  $L$  of the timing graph  $G$ . Reducing  $N_{Is}$  reduces the accuracy of criticality computation but  $L$  has no effect on the accuracy. Moreover, the criticality of a node in  $G$  is equivalent to the sum of its fanin edge or fanout edge criticalities. Exploiting this property, we perform a timing graph reduction (TGR) procedure on nodes with a single fanin or fanout. A straightforward and practical example of this reduction is an inverter chain

Table III shows the effect of TGR on the depth (number of levels)  $L$  and maximum cutset size  $\eta$  on the three largest benchmark circuits. Column 2 shows the size of the circuit. As their names imply, columns w/ red. and w/o red. are results with and without TGR respectively, applied to  $G$ .

TABLE III  
EFFECT OF TGR ON CIRCUIT DEPTH  $L$  AND MAXIMUM CUTSET SIZE  $\eta$

Benchmark	# of Gates	$L$		$\eta$	
		w/o red.	w/ red	w/o red.	w/ red
s38417	22179	51	13	2821	6638
s35932	16065	33	10	5473	10742
s38584	19253	60	19	5680	10374

## VI. RESULTS

Our algorithms were implemented in C++ on top of an SSTA engine and exercised on the ISCAS89 benchmarks, with parameter values corresponding to the 100nm technology node [7]. Experiments were conducted on a Linux PC with a 3.0-GHz CPU and 2GB RAM. We compared four schemes with Monte Carlo simulations using 10000 samples, shown in Table II.

The first scheme is the ZSC approach in Algorithm 2. Scheme nC2 implements the pairwise pruning strategy of Section V-A with a pruning threshold  $\varepsilon$  of 5%. CPSC implements Algorithm 5 using our clustered pruning and ordering technique. Finally, CPSC+LS+TGR performs clustered pruning on the reduced timing graph (TGR) and computes criticalities using the LS procedure with  $N_{Is} = 1000$  samples. Row max.  $\delta$  reports the maximum difference between the edge criticality computed using any of the above mentioned schemes and the Monte Carlo simulations, run-time reports the running time in seconds and  $\eta$  reports the maximum number of edges in any cutset of the timing graph after pruning. We exclude the times for SSTA and generating samples in the LS scheme.

From Table II, ZSC which computes criticalities using Clark's  $MAX_\theta$  formulation results in large errors (the largest being 58%). Since ZSC fundamentally does not differ from [10], we expect to see errors of similar magnitude. CPSC with cutset pruning and ordering does better than ZSC in accuracy and runtime. For circuits exhibiting large global errors, ordering is insufficient to reduce these errors. Rows in bold compare ZSC with CPSC+TGR+LCS. The combined approach greatly reduces errors with negligible increase in runtime compared to CPSC. For the 3 large benchmarks we obtain about an order of magnitude difference run-times of ZSC and the combined approach. Most circuits have errors below 5%, except for s38584. On investigation, it was found that for large fanout structures, path delays themselves (computed in terms of the PCs) contained large errors and hence the LS procedure does not completely eliminate global errors. In terms of the efficacy of our pruning strategy, as expected we vastly outperform the nC2 procedure in run-time (about two orders of magnitude for the larger benchmarks). Moreover, each circuit contained an identical number of edges remaining in the cutsets using the nC2 and CPSC pruning strategies.

## REFERENCES

- [1] H. Chang and S. S. Sapatnekar, "Statistical timing analysis considering spatial correlations using a single PERT-like traversal," in *Proceedings of the 2003 IEEE/ACM International Conference on Computer-Aided Design*. IEEE Computer Society, 2003, p. 621.
- [2] K. Chopra, S. Shah, A. Srivastava, D. Blaauw, and D. Sylvester, "Parametric yield maximization using gate sizing based on efficient statistical power and delay gradient computation," in *Proceedings of the 2005 IEEE/ACM International Conference on Computer-Aided Design*. IEEE Computer Society, 2005, pp. 1023-1028.
- [3] C. E. Clark, "The greatest of a finite set of random variables," *Operations Research*, vol. 9, no. 2, pp. 145-162, Mar-Apr 1961.
- [4] T. F. Gonzalez, "Clustering to minimize the maximum intercluster distance," *Theoretical Computer Science*, vol. 38, no. 2-3, pp. 293-306, 1985.
- [5] X. Li, J. Le, M. Celik, and L. T. Pileggi, "Defining statistical sensitivity for timing optimization of logic circuits with large-scale process and environmental variations," in *Proceedings of the 2005 IEEE/ACM International Conference on Computer-Aided Design*. IEEE Computer Society, 2005, pp. 844-851.
- [6] S. R. Nassif, "Design for variability in DSM technologies," in *Proceedings of the 1st International Symposium on Quality of Electronic Design*. IEEE Computer Society, 2000, p. 451.
- [7] Predictive technology model (PTM). [Online]. Available: <http://www.eas.asu.edu/~ptm/>
- [8] D. Sinha, H. Zhou, and N. V. Shenoy, "Advances in computation of the maximum of a set of random variables," in *Proceedings of the 7th International Symposium on Quality Electronic Design*, Mar. 27-29, 2006.
- [9] C. Visweswariah, K. Ravindran, K. Kalafala, S. G. Walker, and S. Narayan, "First-order incremental block-based statistical timing analysis," in *Proceedings of the 41st Annual Design Automation Conference*. ACM Press, 2004, pp. 331-336.
- [10] J. Xiong, V. Zolotov, N. Venkateswaran, and C. Visweswariah, "Criticality computation in parameterized statistical timing," in *Proceedings of the 43rd Design Automation Conference*. ACM Press, 2006, pp. 63-68.
- [11] T. Yoshimura and E. S. Kuh, "Efficient algorithms for channel routing," *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, vol. 1, no. 1, pp. 25-35, Jan. 1982.